

Polytech Ge Sous Traitance - Task #11778

Feature # 11648 (Closed): Simulation d'un circuit de détection de défauts sous PSIM

Terminer montage et simulation

11/08/2019 02:49 PM - Loic LUCOTTE

Status:	Closed	Start date:	10/23/2019
Priority:	Normal	Due date:	
Assignee:	Haosheng ZHANG	% Done:	0%
Category:		Estimated time:	4.00 hours
Target version:	Deuxième Itération 2019		
Description			

History

#1 - 11/08/2019 02:50 PM - Loic LUCOTTE

- Status changed from New to In Progress

#2 - 11/12/2019 05:39 PM - Loic LUCOTTE

- Status changed from In Progress to Closed

- Remaining (hours) changed from 4.0 to 0.0