

Polytech Ge Sous Traitance - Task #11694

Feature # 11648 (Closed): Simulation d'un circuit de détection de défauts sous PSIM

[ANNULEE] se renseigner

11/04/2019 08:59 PM - Nicolas MISBERT

Status:	Closed	Start date:	10/23/2019
Priority:	Normal	Due date:	
Assignee:	Yousef TOUGHACH	% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:	Deuxième Itération 2019		
Description			

History

#1 - 11/05/2019 01:46 PM - Loic LUCOTTE

- Status changed from New to Closed
- Remaining (hours) set to 0.0

#2 - 11/05/2019 01:49 PM - Loic LUCOTTE

- Estimated time set to 0.00

#3 - 11/12/2019 05:39 PM - Loic LUCOTTE

- Subject changed from SE RENSEIGNER to [ANNULEE] se renseigner